Iterated multiplication in VTC⁰

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Outline

- **1** \mathbf{TC}^0 , VTC^0 , and *IMUL*
- 2 Hesse–Allender–Barrington algorithm
- **3** Working with CRR
- 4 Polylogarithmic cut
- 5 Modular exponentiation
- 6 The grand scheme

TC⁰, *VTC*⁰, and *IMUL*

1 \mathbf{TC}^{0} , VTC^{0} , and IMUL

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- **3** Working with CRR
- **4** Polylogarithmic cut
- **(5)** Modular exponentiation
- **6** The grand scheme

Theories vs. complexity classes

Correspondence of theories of bounded arithmetic T and computational complexity classes C:

- Provably total computable functions of T are C-functions
- T can do reasoning using C-predicates (comprehension, induction, ...)

Feasible reasoning:

- ► Given a natural concept X ∈ C, what can we prove about X using only concepts from C?
- ▶ That is: what does *T* prove about *X*?

This talk:

X = elementary integer arithmetic operations $+, \cdot, \leq$

$\mathsf{AC}^0 \subseteq \mathsf{ACC}^0 \subseteq \mathsf{TC}^0 \subseteq \mathsf{NC}^1 \subseteq \mathsf{L} \subseteq \mathsf{NL} \subseteq \mathsf{AC}^1 \subseteq \cdots \subseteq \mathsf{P}$

 $TC^{0} = dlogtime-uniform O(1)-depth n^{O(1)}-size$ unbounded fan-in circuits with threshold gates = FOM-definable on finite structures representing strings (first-order logic with majority quantifiers) = O(log n) time, O(1) thresholds on a threshold Turing machine

TC⁰ and arithmetic operations

For integers given in binary:

- ▶ + and ≤ are in $AC^0 \subseteq TC^0$
- \blacktriangleright × is in **TC**⁰ (**TC**⁰-complete under **AC**⁰ reductions)

 \mathbf{TC}^0 can also do:

- iterated addition $\sum_{i < n} X_i$
- integer division and iterated multiplication [BCH'86,CDL'01,HAB'02]
- the corresponding operations on \mathbb{Q} , $\mathbb{Q}(i)$
- approximate functions given by nice power series:

 $\blacktriangleright \ \sin X, \ \log X, \ \sqrt[k]{X}, \ \dots$

sorting, ...

 \implies **TC**⁰ is the right class for basic arithmetic operations

Buss-style bounded arithmetic

One-sorted theories of bounded arithmetic:

- ▶ language $\langle 0, 1, +, \cdot, \leq, \lfloor x/2 \rfloor, |x|, \# \rangle$
- Σ_0^b formulas: sharply bounded q'fiers $\exists x \leq |t|$, $\forall x \leq |t|$

•
$$T_2^i = BASIC + \hat{\Sigma}_i^b$$
-IND, $S_2^i = BASIC + \hat{\Sigma}_i^b$ -LIND

$$T_2 = \bigcup_i T_2^i = \bigcup_i S_2^i \cong I \Delta_0 + \Omega_1$$

Johannsen and Pollett's theories for **TC**⁰:

- ▶ language with -, $\lfloor x/2^y \rfloor$
- all theories include open LIND
- $C_2^0: BB\Sigma_0^b$ [JP'98]
- $C_2^0[div]$: language incl. $\lfloor x/y \rfloor$ [Joh'99]
- Δ_1^b -*CR*: Δ_1^b bit-comprehension rule [JP'00]

Zambella-style bounded arithmetic

Two-sorted bounded arithmetic:

- unary (auxiliary) integers with $0, 1, +, \cdot, \leq$
- Finite sets = binary integers = binary strings x ∈ X, |X| = sup{x + 1 : x ∈ X}
- ▶ bounded quantifiers: $\exists x \leq t, \forall x \leq t, \exists X \leq t, \forall X \leq t$ where $X \leq t$ is short for $|X| \leq t$
- \triangleright Σ_0^B formulas: bounded FO, no SO quantifiers
- ► $\sum_{i=1}^{B}$ formulas: *i* alternating blocks of bounded quantifiers (first block \exists) followed by a $\sum_{i=1}^{B}$ formula
- $V^i = 2$ -BASIC + Σ^B_i -COMP (implies Σ^B_i -IND)

The theory VTC^0

The two-sorted theory corresponding to \mathbf{TC}^0 is VTC^0 :

- V^0 + every set has a counting function
- Provably total computable (i.e., ∃Σ₀^B-definable) functions are exactly the TC⁰-functions
- has induction, comprehension, minimization, ... for TC⁰-predicates

Binary arithmetic in VTC^0 :

- ▶ can define $+, \cdot, \leq$ on binary integers
- proves integers form a discretely ordered ring
- iterated multiplication challenging \implies axiom *IMUL*:

$$\forall X, n \exists Y \forall i \leq j < n (Y_{i,i} = 1 \land Y_{i,j+1} = Y_{i,j} \cdot X_j)$$

(think
$$Y_{i,j} = \prod_{k=i}^{j-1} X_k$$
)

RSUV isomorphism

two-sorted arithmetic	one-sorted arithmetic
sets	numbers
numbers	logarithmic numbers
bounded SO quantifiers	bounded quantifiers
bounded FO quantifiers	sharply bounded quantifiers
Σ_i^B	$\hat{\Sigma}_{i}^{b}$
V^i	S_2^i
ΤV ⁱ	T_2^i
VTC ⁰	Δ_1^b -CR
$VTC^0 + \Sigma_0^B - AC$	C_{2}^{0}
$VTC^{0} + IMUL + \Sigma_{0}^{B}-AC$	$C_2^0[div]$
$(i \ge 1)$	

Arithmetic in $VTC^0 + IMUL / C_2^0[div]$

Besides division, $VTC^0 + IMUL / C_2^0[div]$ can do:

- root approximation for constant-degree polynomials
- $\blacktriangleright \implies (RSUV \text{-translation of}) \text{ open induction } (IOpen)$

Even better (using ideas of [Man'91]):

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Theorem [J'15]
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- ► $VTC^0 + IMUL$ proves the *RSUV*-translations of Σ_0^b -IND (T_2^0) and Σ_0^b -MIN
- $C_2^0[div]$ proves Σ_0^b -IND, Σ_0^b -MIN

What remains

Question

Does VTC⁰ prove IMUL?

Iterated multiplication is **TC**⁰-computable:

Question

Can VTC^0 formalize the algorithms from [HAB'02]?

Hesse–Allender–Barrington algorithm

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History

[BCH'86]

- $\prod_{i < n} X_i, \lfloor Y / X \rfloor, X^n \text{ are } \mathbf{TC}^0 \text{-reducible to each other}$
- they are in P-uniform TC⁰

compute the product in Chinese remainder representation:

 $\operatorname{CRR}_{\vec{m}}(X) = \langle X \mod m_i : i < k \rangle$

where $\vec{m} = \langle m_i : i < k \rangle$ small primes

▶ (NB: predates definition of **TC**⁰)

Improved CRR reconstruction procedures \implies

- ▶ [CDL'01]: logspace-uniform **TC**⁰ (hence **L**)
- ► [HAB'02]: dlogtime-uniform **TC**⁰

Structure of the algorithm

(1) $\prod_{u < t} X_u$ is in **TC**⁰[pow]

• pick sufficiently long list of primes \vec{m}

• convert each X_u to $CRR_{\vec{m}}$

- multiply the residues modulo each m_i
- reconstruct the result from $CRR_{\vec{m}}$ to binary

(2)
$$\prod_{u < t} X_u$$
 is in AC^0 if $\sum_{u < t} |X_u| = (\log n)^{O(1)}$

scale (1) down

(3) pow is in AC^0

• express exponents in $CRR_{\vec{d}}$

pow: $a^r \mod m$ (a, r unary, m unary prime)

Structure of the algorithm

(0) imul is in **TC**⁰[pow] sum discrete logarithms modulo m (1) $\prod_{u \leq t} X_u$ is in **TC**⁰[imul] \blacktriangleright pick sufficiently long list of primes \vec{m} \blacktriangleright convert each X_{μ} to $CRR_{\vec{m}}$ multiply the residues modulo each m_i • reconstruct the result from $CRR_{\vec{m}}$ to binary (2) $\prod_{u < t} X_u$ is in AC^0 if $\sum_{u < t} |X_u| = (\log n)^{O(1)}$ scale (1) down (3) pow is in AC^0 • express exponents in $CRR_{\vec{d}}$

imul: $\prod_{i < n} a_i \mod m$ (*n*, a_i unary, *m* unary prime)

Obstacles to formalization

Complex structure with interdependent parts

Which came first: the chicken or the egg?

- $CRR_{\vec{m}}$ reconstruction:
 - ► analysis heavily uses iterated products and divisions: $\prod_{i < k} m_i, \ldots$
 - need $CRR_{\vec{m}}$ reconstruction to define iterated products and divisions in the first place
- computation of pow:
 - analysis of the pow algorithm heavily uses pow
 - relies on Fermat's little theorem

► cyclicity of (Z/pZ)[×]:

- needed to compute imul in TC⁰[pow]
- notoriously difficult in bounded arithmetic
- provable in $VTC^0 + IMUL$, but what good is that?

Results [J'20]

Theorem

 $VTC^0 \vdash IMUL$

Corollary

- ► $VTC^0 \vdash RSUV$ -translation of Σ_0^b -MIN
- $C_2^0 \equiv C_2^0[div]$, proves Σ_0^b -MIN

Theorem

$$\exists \Delta_0 \text{ definition of } a^r \mod m \text{ s.t. } I\Delta_0 + WPHP(\Delta_0) \vdash a^0 \equiv 1 \pmod{m}, \qquad a^{r+1} \equiv a^r a \pmod{m}$$

Overview of the formalization



- ▶ VTC^0 \vdash there are enough primes
- $VTC^0(pow)$ can do division $\lfloor X/m \rfloor$ by small primes
- (1) $VTC^{0}(\text{imul}) \vdash IMUL$
 - ► hard part: CRR reconstruction
 - teach $VTC^{0}(\text{imul})$ to compute in CRR from scratch
- (2) $V^0 \vdash IMUL[|w|^c]$
 - the polylogarithmic cut in V^0 is a model of VNL
- (3) $V^0 + WPHP \vdash$ totality of pow
 - reorganize the [HAB'02] algorithm to avoid circularity
 - can't do (0) directly!
 - structure theorem for finite abelian groups (partially)
 - each turn around the vicious circle
 IMUL → cyclicity → imul → *IMUL* makes progress
 ⇒ proof by induction

Working with CRR

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Goal: CRR reconstruction

Theorem

 $\exists \mathbf{TC}^{0}(\text{imul})\text{-function Rec s.t. } VTC^{0}(\text{imul}) \text{ proves:} \\ \vec{m} \text{ distinct primes, } |X| < \sum_{i} (|m_{i}| - 1) \\ \implies \operatorname{Rec}(\vec{m}; \operatorname{CRR}_{\vec{m}}(X)) = X$

Corollary

 $VTC^{0}(\text{imul}) \vdash IMUL$

Proof: \vec{m} large enough $\implies Y_j := \operatorname{Rec}(\vec{m}; \prod_{i < j} \operatorname{CRR}_{\vec{m}}(X_i))$ By induction on j, show $|Y_j| \le \sum_{i < j} |X_i|$ and $Y_{j+1} = X_j Y_j$

Basic tool

Notation:
$$[ec{m}] = \prod_{i < k} m_i$$
, $[ec{m}]_{
eq j} = \prod_{i
eq j} m_i$

CRR rank equation

$$X < [\vec{m}], \, \vec{x} = \operatorname{CRR}_{\vec{m}}(X) \implies$$

$$\sum_{i \le k} \frac{x_i h_i}{m_i} = r(\vec{x}) + \frac{X}{[\vec{m}]}$$

where $h_i = [\vec{m}]_{\neq i}^{-1} \mod m_i$

▶ rank $r(\vec{x})$: small integer

▶ holds in $\mathbb{Q} \implies$ approximation $\xi(\vec{m}; \vec{x})$ of $X/[\vec{m}]$

▶ holds in $\mathbb{Z}/a\mathbb{Z} \implies$ base extension $e(\vec{m}; \vec{x}; a) = X \mod a$

Rank and friends formalized

In VTC^0 (imul): for large enough *n*, consider

$$S_n(\vec{m}; \vec{x}) = \sum_{i < k} \left[\frac{2^n x_i h_i}{m_i} \right]$$
$$r_n(\vec{m}; \vec{x}) = \lfloor 2^{-n} S_n(\vec{m}; \vec{x}) \rfloor$$
$$\xi_n(\vec{m}; \vec{x}) = 2^{-n} \left(S_n(\vec{m}; \vec{x}) \mod 2^n \right)$$
$$e_n(\vec{m}; \vec{x}; a) = \sum_{i < k} x_i h_i [\vec{m}]_{\neq i} - r_n(\vec{m}; \vec{x}) [\vec{m}] \mod a$$

The laborious part:

- ▶ prove lots of properties of r_n , ξ_n , e_n from first principles
- use them to analyze the reconstruction procedure

Polylogarithmic cut

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The polylogarithmic cut

$$\mathcal{M} = \langle M_1, M_2, \in, |\cdot|, 0, 1, +, \cdot, < \rangle \vDash V^0$$

$$\implies \mathcal{M}_{\mathrm{pl}} = \langle M_{\mathrm{pl},1}, M_{\mathrm{pl},2}, \dots \rangle \text{ where}$$

$$M_{\mathrm{pl},1} = \{ x \in M_1 : \exists c \in \omega \ \mathcal{M} \vDash \exists w \ x \le |w|^c \}$$

$$M_{\mathrm{pl},2} = \{ X \in M_2 : |X| \in M_{\mathrm{pl},1} \}$$

Using the idea of Nepomnjaščij's theorem:

- $\blacktriangleright \text{ [Zam'97] (implicitly) } \mathcal{M} \vDash V^0 \implies \mathcal{M}_{\text{pl}} \vDash VL$
- $\blacktriangleright \text{ [Mül'13] } \mathcal{M} \vDash V^0 \implies \mathcal{M}_{\text{pl}} \vDash VNC^1$
- similar formalization in [Ats'03]

Lemma

 $\mathcal{M} \vDash V^0 \implies \mathcal{M}_{\mathrm{pl}} \vDash VNL$

Polylogarithmic products

Lemma

$$VTC^{0}(\text{imul}) \subseteq VL$$

Corollary

For any constant c, V^0 can do:

$$\prod_{i < n} X_i \text{ if } \sum_i |X_i| \le |w|^c$$

$$\lfloor Y/X \rfloor \text{ if } |X|, |Y| \le |w|^c$$

•
$$\prod_{i < n} a_i \mod m$$
 if $n \le |w|^c$

Modular exponentiation

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The [HAB'02] algorithm

To compute a^r for $a \in (\mathbb{Z}/m\mathbb{Z})^{\times}$, $n = \varphi(m) = |(\mathbb{Z}/m\mathbb{Z})^{\times}|$:

$$r \equiv u + \sum_{i} u_i \left\lfloor \frac{n}{d_i} \right\rfloor \pmod{n}$$

• using $a^n = 1$, compute $a_i = a^{\lfloor n/d_i \rfloor} = a^{-(n \mod d_i)/d_i}$, $a^r = a^u \prod_i a_i^{u_i}$

Analysis requires: modular exponentiation (chicken or egg?), Fermat's little theorem

Drop $a^{\lfloor n/d_i \rfloor}$, just use a^{1/d_i} directly

To compute a^r for $a \in (\mathbb{Z}/m\mathbb{Z})^{\times}$, $n = \varphi(m) = |(\mathbb{Z}/m\mathbb{Z})^{\times}|$:

$$\frac{s}{d} = u + \sum_{i} \frac{u_i}{d_i}$$
 (CRR_{*d̃*} rank equation)

Modular exponentiation formalized

Theorem

$$V^0 + WPHP \subseteq VTC^0$$
 proves the totality of pow

Also extends to non-prime m

Using conservativity, can do it in $I\Delta_0 + WPHP(\Delta_0)$:

Theorem

 $\exists \Delta_0$ definition of $a^r \mod m$ s.t. $I\Delta_0 + WPHP(\Delta_0) \vdash$

$$a^0 \equiv 1 \pmod{m},$$

 $a^{r+1} \equiv a^r a \pmod{m}$

The grand scheme

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Cyclic generators

Still missing:
$$VTC^0 \stackrel{?}{\vdash} m$$
 prime $\rightarrow (\mathbb{Z}/m\mathbb{Z})^{\times}$ is cyclic
 $\implies VTC^0 = VTC^0(\text{pow}) = VTC^0(\text{imul})$



Can we escape this vicious circle?

Fine-tune the parameters:

► IMUL[x], imul[x], Cyc[z, x]

Fine-tune the parameters:

 $\blacktriangleright IMUL[x], imul[x], Cyc[z, x]$

 $\exists \prod_{i < n} X_i$ whenever $\sum_i |X_i| \le x$

Fine-tune the parameters:

 $\blacktriangleright IMUL[x], imul[x], Cyc[z, x]$

 $\exists \prod_{i < n} a_i \mod m$ whenever $m \leq x$ prime

Fine-tune the parameters:

► IMUL[x], imul[x], Cyc[z, x] ($Cyc \in \Sigma_0^B$)

 $m \le z \text{ and } p < x \text{ primes, } a \not\equiv 1 \equiv a^p \equiv b^p \pmod{m}$ $\implies \exists r$

Fine-tune the parameters:

IMUL[x], imul[x], Cyc[z, x] (Cyc ∈ Σ₀^B)
 *VTC*⁰ proves

$$\begin{aligned} \operatorname{imul}[x^3] &\to IMUL[x] \\ IMUL[x^2|z|] &\to Cyc[z,x] \\ Cyc[z,x] &\to \operatorname{imul}[\min\{z,x^c|z|^c\}] \end{aligned}$$

(new idea: structure theorem for finite abelian groups)

$$\therefore (x+1)^{6}|z|^{3} \leq z \land \textit{Cyc}[z,x] \rightarrow \textit{Cyc}[z,x+1]$$

finish the proof by induction on x

Summary

- VTC⁰ proves IMUL
- VTC^0 proves *RSUV*-translation of Σ_0^b -*MIN*
- $C_2^0 \equiv C_2^0[div]$, proves Σ_0^b -MIN
- IΔ₀ + WPHP(Δ₀) has a well-behaved
 Δ₀ definition of a^r mod m

References

- A. Atserias: Improved bounds on the Weak Pigeonhole Principle and infinitely many primes from weaker axioms, Theoret. Comput. Sci. 295 (2003), 27–39
- P. Beame, S. Cook, H. Hoover: Log depth circuits for division and related problems, SIAM J. Comp. 15 (1986), 994–1003
- A. Chiu, G. Davida, B. Litow: Division in logspace-uniform NC¹, RAIRO – Theoret. Inf. Appl. 35 (2001), 259–275
- S. Cook, P. Nguyen: Logical foundations of proof complexity, Cambridge Univ. Press, 2010
- W. Hesse, E. Allender, D. M. Barrington: Uniform constant-depth threshold circuits for division and iterated multiplication, J. Comp. System Sci. 65 (2002), 695–716
- E. Jeřábek: Open induction in a bounded arithmetic for TC⁰, Arch. Math. Logic 54 (2015), 359–394

References (cont'd)

- E. Jeřábek: Iterated multiplication in VTC⁰, arXiv:2011.03095
- J. Johannsen, C. Pollett: On proofs about threshold circuits and counting hierarchies (extended abstract), LICS, 1998, 444–452
- ► J. Johannsen: Weak bounded arithmetic, the Diffie-Hellman problem, and Constable's class *K*, LICS, 1999, 268–274
- J. Johannsen, C. Pollett: On the Δ₁^b-bit-comprehension rule, Logic Colloquium '98 (Proceedings), ASL, 2000, 262–280
- S.-G. Mantzivis: Circuits in bounded arithmetic part I, Ann. Math. Artif. Intel. 6 (1991), 127–156
- S. Müller: Polylogarithmic cuts in models of V⁰, Logical Methods in Comp. Sci. 9 (2013), no. 1
- D. Zambella: End extensions of models of linearly bounded arithmetic, Ann. Pure Appl. Logic 88 (1997), 263–277